

IEDC project No. 2 – for the year 2016 -2017

KAKSA - Intelligence in Class Boards



Narendra Santhosh N



Naveen Kumar R



Ajay Karthik K



Athmanathan S

FINAL YEAR ELECTRONICS AND COMMUNICATION ENGINEERING

SRI SAIRAM INSTITUTE OF TECHNOLOGY



Guided By: Mr. N. Oral Roberts

Asst. Professor

Electronics and Communication Engineering Department

Sri Sai Ram Institute of Technology

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NATIONAL SCIENCE & TECHNOLOGY ENTREPRENEURSHIP DEVELOPMENT

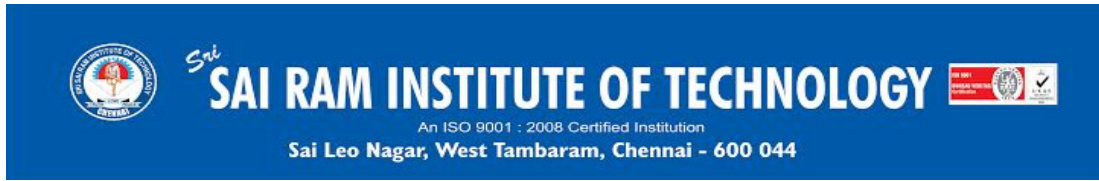
BOARD (NSTEDB),

DEPARTMENT OF SCIENCE AND TECHNOLOGY,

GOVERNMENT OF INDIA, NEW DELHI.



DEPARTMENT OF
SCIENCE AND TECHNOLOGY



IEDC project No. 06 – for the year 2017 -2018

SMART BUS STOP



Aishwarya.S



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Swetha Ganesh



Dhara Sravani

Final year B.E Electronics and Communication Engineering

Guided by



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Sri **SAI RAM INSTITUTE OF TECHNOLOGY**

An ISO 9001 : 2008 Certified Institution

Sai Leo Nagar, West Tambaram, Chennai - 600 044



INNOVATION AND ENTREPRENEURSHIP DEVELOPMENT CENTRE (IEDC)

(Funded by Department of Science & Technology, Government of India, New Delhi)

IEDC project No. 04 – for the year 2015 -2016

CLAP SENSOR FOR DIFFERENTLY ABLED AND AGED PEOPLE



S. Mythili



R. Nagammai Nachu



S. Malathy



V. Kayalvizhi

IV YEAR ELECTRONICS AND COMMUNICATION ENGINEERING
SRI SAIRAM INSTITUTE OF TECHNOLOGY



Guide: **Dr. S. Rajarajan**

Professor/ECE

SRI SAIRAM INSTITUTE OF TECHNOLOGY

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